



Title: ESyst: Electronics Systems Technology Project

Date/Time: Tuesday, July 29, 8:30 AM – 4:30 PM

Location: Renaissance Austin Hotel

Cost: \$160

Description: Join us for the ESyst Project presented by Tom McGlew of the Maricopa Advanced Technology Education Center (MATEC) and members of the ESyst development team. This full-day workshop is the second in a series of ongoing events designed to highlight and inform you on the status of the new National Science Foundation funded ATE project grant (DUE-0702753) “A New Systems View of Electronics for 2010”. This project’s deliverables will address declining enrollments in your current electronics programs, will help to revitalize your programs through a “systems” approach to teaching electronics, and will aid in retaining your students.

In the morning, the ESyst panel of developers will update you on the current status of the project including: development activities on the first three courses to be updated to a systems approach, the process for developing Concept Inventory evaluation tools, and a look at the Online Systems Lab activity developed with the Massachusetts Institute of Technology.

In the afternoon, the ESyst panel will present overviews of the first three modified courses and their associated systems lab activities. Be the first to see these new courses and lab activities and ask your questions directly to the course developers on how they should be implemented. We look forward to sharing with you the progresses of the project and our insights gained along the way toward developing a systems approach to teaching electronics technology.

Presenters:



Thomas McGlew has more than 30 years of experience in the fields of semiconductor manufacturing and employee development. Tom has served as one of the founding Committee Members of the Northwest Semiconductor Workforce Development Consortium, as a Steering Member for the Arizona SEMI Committee, and as a Member of the American Society for Training and Development (ASTD). He is the Instructional Program Development Specialist at MATEC, with Project Lead responsibilities for the ESyst project. Mr. McGlew is a certified instructor for numerous Management and Leadership Development Workshops and has spoken at two ASTD Technical Education Conferences on Mentoring in the Workplace.



Jim Hyder, M.S., is a Training Program Manager/Instructional Designer aligned to Diffusion, Implant, and Rapid Thermal Anneal at Intel's Fab 11X. Prior to this, he served as a Manufacturing Supervisor for numerous functional areas throughout the factory and as a Training Supervisor. Jim is currently a Ph.D. Learner at Capella University's School of Education specializing in Training and Performance Improvement. He holds a Bachelor of Science Degree majoring in Workforce Education and Curriculum Development from Southern Illinois University and a Master of Science Degree of Management in Science and Technology from Oregon

Health and Sciences University.



Louis E. Frenzel, M.Ed., is a Technology Editor for *Electronic Design* Magazine where he writes articles, columns, technology reports, and online material on the wireless, networking, and test/measurement sectors. He interviews executives and engineers, attends conferences, researches those areas of electronics to determine the current state of technology, and reviews new products. Lou has been with the magazine for seven years. Formerly, he was professor and department head at Austin Community College (ACC) where he taught electronics for five years. He still teaches at ACC as an Adjunct Professor. Lou has 25+ years of experience in the

electronics industry. He holds a bachelor's degree from the University of Houston and a master's degree from the University of Maryland. He is the author of 19 books on computer and electronic subjects. He has worked with MATEC as a contractor for five years on several National Science Foundation grants and is the principal author of the Work-Ready Electronics series. Lou was also the recipient of the 2007 MATEC Industry Recognition Award.



Wayne Phillips is a motivated Electronics and Computer Technology educator at Chabot College, with years of experience in development and delivery of technical programs and courses. Wayne is skilled in leading and working in groups of diverse talents to address complex technical issues. He also has extensive experience with complex systems, including electronic, software, precision mechanical, industrial automation, high vacuum, electronoptic, laser, and environmental systems.