

# System Overview

## MODULE 118

Learning Activities	Time in Class	Outside Time
6	5.5 hrs	2 hrs

### OVERVIEW

Technicians face the challenge of building and maintaining knowledge and skills that are applicable to microcomputer architecture. These challenges include keeping up to date on improvements in PC components, changes in PC hardware configurations, integration of new devices within PC systems, and the ability to readily apply their knowledge within the microcomputer manufacturing industry. This module provides an introduction and overview to the major system features that define the architecture of an Intel-based PC. A unique multimedia self-tutorial is included.

### MODULE CONTENTS



#### Narrative Overview

- Learning Activity 01: System Overview
- Learning Activity 02: Memory and I/O Maps
- Learning Activity 03: BIOS and POST
- Learning Activity 04: Bus Standards: ISA and PCI
- Learning Activity 05: Internet Research on PC Architecture
- Learning Activity 06: PC Architecture Questor Game
- Slide Show 01: PC Systems Overview
- Slide Show 02: Memory and I/O Maps
- Slide Show 03: BIOS and POST
- Slide Show 04: Bus standards: ISA and PCI
- Performance Assessment 01:
- Animation 01: "The Questor Game"



MATEC is a member of the Academic Affairs Division of the Maricopa Community Colleges



MATEC — Since 1996, the National Science Foundation's U. S. Center of Excellence for Education in Semiconductors, Automated Manufacturing, and Electronics



[Click Here for Pricing & Ordering Information](#)